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From : Winston Hsu, Registration No. 41,526

Serial No.: 10/605,015

Attorney Docket No.: ADTP0094USA

Subject: Information Disclosure Statement (IDS)

Total Pages: 30 pages (including cover page)

Winston Hsu Date: JUL 17 2006

ADTP0094USA0_D1_1

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PTO/SB/21 (09-04)

Approved for use through 07/31/2006. OMB 0651-0031

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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TRANSMITTAL FORM <small>(to be used for all correspondence after initial filing)</small>	Application Number	10/605,015	
	Filing Date	09/01/2003	
	First Named Inventor	Chih-Chin Chang	
	Art Unit	2811	
	Examiner Name	ARENA, ANDREW OWENS	
Total Number of Pages in This Submission	29	Attorney Docket Number	ADTP0094USA

ENCLOSURES (Check all that apply)		
<input checked="" type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment/Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Reply to Missing Parts/Incomplete Application <input type="checkbox"/> Reply to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation <input type="checkbox"/> Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD Number of CD(s) _____ <input type="checkbox"/> Landscape Table on CD	<input type="checkbox"/> After Allowance Communication to TC <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input type="checkbox"/> Other Enclosure(s) (please identify below):
Remarks: _____		
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm Name	North America Intellectual Property Corporation	
Signature	<i>Winston Hsu</i>	
Printed name	Winston Hsu	
Date	JUL 17 2006	Reg No 41,526

CERTIFICATE OF TRANSMISSION/MAILING		
I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents P.O. Box 1450 Alexandria VA 22313-1450 on the date shown below:		
Signature	<i>Jun Yu</i>	
Typed or printed name	Jun Yu	Date JUL 17 2006

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PTO/SB/17 (12-04v2)

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Effective on 12/08/2004.
Fees pursuant to the Consolidated Appropriations Act, 2005 (H.R. 4918)

FEE TRANSMITTAL For FY 2005

☐ Applicant claims small entity status See 37 CFR 1.27

TOTAL AMOUNT OF PAYMENT (\$) 0.00

Complete if Known

Application Number	10/605,015
Filing Date	09/01/2003
First Named Inventor	Chih-Chin Chang
Examiner Name	ARENA, ANDREW OWENS
Art Unit	2811
Attorney Docket No.	ADTP0094USA

METHOD OF PAYMENT (check all that apply)

☐ Check ☐ Credit Card ☐ Money Order ☐ None ☐ Other (please identify): _____
☒ Deposit Account Deposit Account Number: 50-3105 Deposit Account Name: North America Intellectual Property Corporation
 For the above-identified deposit account, the Director is hereby authorized to: (check all that apply)
☒ Charge fee(s) indicated below ☐ Charge fee(s) indicated below except for the filing fee
☒ Charge any additional fee(s) or underpayments of fee(s) under 37 CFR 1.16 and 1.17 ☒ Credit any overpayments

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FEE CALCULATION

1. BASIC FILING, SEARCH, AND EXAMINATION FEES

Application Type	FILING FEES		SEARCH FEES		EXAMINATION FEES		Fees Paid (\$)
	Small Entity Fee (\$)	Fee (\$)	Small Entity Fee (\$)	Fee (\$)	Small Entity Fee (\$)	Fee (\$)	
Utility	300	150	500	250	200	100	
Design	200	100	100	50	130	65	
Plant	200	100	300	150	160	80	
Reissue	300	150	500	250	600	300	
Provisional	200	100	0	0	0	0	

2. EXCESS CLAIM FEES

Fee Description	Small Entity Fee (\$)	Fee (\$)
Each claim over 20 (including Reissues)	50	25
Each independent claim over 3 (including Reissues)	200	100
Multiple dependent claims	360	180

Total Claims **Extra Claims** **Fee (\$)** **Fee Paid (\$)** **Multiple Dependent Claims**
 _____ - 20 or HP = _____ x _____ = _____
 HP = highest number of total claims paid for, if greater than 20
Indep. Claims **Extra Claims** **Fee (\$)** **Fee Paid (\$)**
 _____ - 3 or HP = _____ x _____ = _____
 HP = highest number of independent claims paid for, if greater than 3

3. APPLICATION SIZE FEE

If the specification and drawings exceed 100 sheets of paper (excluding electronically filed sequence or computer listings under 37 CFR 1.52(e)), the application size fee due is \$250 (\$125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).

Total Sheets **Extra Sheets** **Number of each additional 50 or fraction thereof** **Fee (\$)** **Fee Paid (\$)**
 _____ - 100 = _____ / 50 _____ (round up to a whole number) x _____ = _____

4. OTHER FEE(S)

Non-English Specification, \$130 fee (no small entity discount)
 Other (e.g., late filing surcharge): submission of Information Disclosure Statement **Fees Paid (\$)** 0.00

SUBMITTED BY			
Signature	<i>Winston Hsu</i>	Registration No. (Attorney/Agent)	41,526
Name (Print/Type)	Winston Hsu	Telephone	3027291562
		Date	JUL 17 2006

This collection of information is required by 37 CFR 1.106. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 30 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Chih-Chin Chang et al.
Appl. No.: 10/605,015 Filing Date: 09/01/2003
Examiner: ARENA, ANDREW OWENS Art Unit: 2811
Docket No.: ADTP0094USA Confirmation No.: 2014

Title: STACKED CAPACITOR HAVING PARALLEL INTERDIGITIZED
STRUCTURE FOR USE IN THIN FILM TRANSISTOR LIQUID
CRYSTAL DISPLAY

To: Commissioner for Patents
P.O. BOX 1450
Alexandria, VA 22313-1450

Subject: Information disclosure statement under 37 CFR §1.56

Dear Sir or Madame:

This is an Information Disclosure Statement in accordance with the duty to disclose information material to patentability under 37 CFR §1.56. The applicant wishes to make of record the document(s) listed on the accompanying form PTO/SB/08.

Since this IDS is filed before the mailing date of a first Office action after the filing of a request for continued examination under §1.114, consideration of the information disclosure statement is hereby requested according to 37 CFR §1.97(b)(4). That each item of information contained in the information disclosure statement was first cited in an Office communication mailed on 04/28/2006 for the counterpart Chinese application number 03147450.0, which are no more than three months prior to the filing of the information disclosure

1/3

statement.

The abstract of the application (JP H09-162412) is hereby presented to fulfill the information disclosure statement requirement set forth in 37 C.F.R. §1.98 and M.P.E.P. 609. The English language abstract of the application (JP H09-162412) is listed as follows:

PROBLEM TO BE SOLVED: To display a high quality image by increasing a pixel opening ratio and also reducing the resistance of capacitance line, and further reducing cross-talk and ghost in an electro-optical device.

SOLUTION: The electro-optical device is provided with pixel electrodes (9a), TFTs (30) for controlling to switch the pixel electrodes, scanning lines (3a) connected with these TFTs, and 2nd capacitance lines (3b) for adding storage capacitance (70) on a TFT array substrate (10). The device is further provided with a barrier layer (80) for relay-connecting the pixel electrodes to the TFTs and 1st capacitance lines (82).

The abstract of the application (JP 2001-265253) is hereby presented to fulfill the information disclosure statement requirement set forth in 37 C.F.R. §1.98 and M.P.E.P. 609. The English language abstract of the application (JP 2001-265253) is listed as follows:

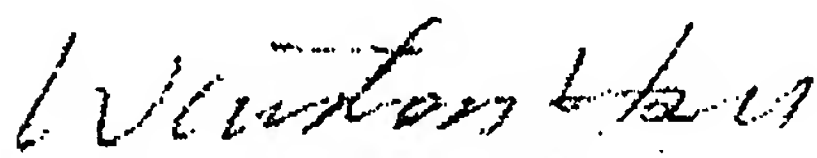
PROBLEM TO BE SOLVED: To provide a thin film transistor, used for an active matrix liquid crystal panel, in which leakage current is reduced, and a thin film transistor array which is provided with the thin film transistor and forms a large storage capacity with a small area.

SOLUTION: An active layer 207 is put between the first gate electrode 203 and the second gate electrode 209, and an off-set area 206 is overlapped with the third gate electrode 202 and the fourth gate electrode 211, so that high on-current and low leakage current are realized at the same time. The storage capacities suppressing dropping of electric potential of a pixel are formed between a transparent pixel electrode 223 and the third electrode 22 for storage capacity, the third electrode 222 for storage capacity and the second electrode 221 for storage capacity, and between the second electrode 221 for storage capacity and the first electrode 220 for storage capacity, so that, a large storage capacity is formed with small areas.

It is respectfully requested that the examiner can consider the document(s) listed on the accompanying form PTO/SB/08 and that it be made of record in the application.

The applicants sincerely hope that the examiner initials the cited reference(s) on the form and that a copy of the initialed form be sent to the applicants with the next communication from the examiner.

Respectfully submitted,



Date: JUL 17 2006

Winston Hsu, Patent Agent No. 41,526

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(The time in D.C. is 12 hours behind the Taiwan time, i.e. 9 AM in D.C.
= 9 PM in Taiwan.)